General Description

Features

Accuracy

The MAX6627/MAX6628 precise digital temperature sensors report the temperature of a remote sensor. The remote sensor is a diode-connected transistor, typically a low-cost, easily mounted 2N3904 NPN type that replaces conventional thermistors or thermocouples. The MAX6627/MAX6628 can also measure the die temperature of other ICs, such as microprocessors (µPs) or microcontrollers (µCs) that contain an on-chip, diodeconnected transistor.

Remote accuracy is $\pm 1^{\circ}$ C when the temperature of the remote diode is between 0°C and +125°C and the temperature of the MAX6627/MAX6628 is +30°C. The temperature is converted to a 12-bit + sign word with 0.0625°C resolution. The architecture of the device is capable of interpreting data as high as +145°C from the remote sensor. The MAX6627/MAX6628 temperature should never exceed +125°C.

These sensors are 3-wire serial interface SPI™ compatible, allowing the MAX6627/MAX6628 to be readily connected to a variety of µCs. The MAX6627/MAX6628 are read-only devices, simplifying their use in systems where only temperature data is required.

Two conversion rates are available, one that continuously converts data every 0.5s (MAX6627), and one that converts data every 8s (MAX6628). The slower version provides minimal power consumption under all operating conditions (30µA, typ). Either device can be read at any time and provide the data from the last conversion.

Both devices operate with supply voltages between +3.0V and +5.5V, are specified between -55°C and +125°C, and come in the space-saving 8-pin SOT23 package.

Applications

1-888-629-4642, or visit Maxim's website at www.maxim-ic.com.

Hard Disk Drive Smart Battery Packs Automotive Industrial Control Systems Notebooks, PCs

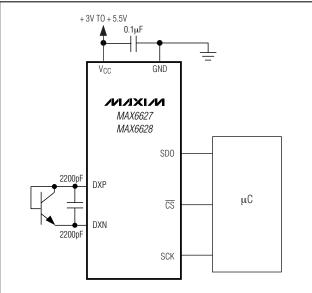
 $\pm 1^{\circ}C$ (max) from $0^{\circ}C \leq T_{RJ} \leq \pm 125^{\circ}C$, $T_{A} = \pm 30^{\circ}C$ $\pm 2.4^{\circ}C$ (max) from $-55^{\circ}C \leq T_{BJ} \leq +100^{\circ}C$, $0^{\circ}C \leq T_{A} \leq +70^{\circ}C$

- 12-Bit + Sign, 0.0625°C Resolution
- Low Power Consumption 30µA (typ) (MAX6628) 200µA (typ) (MAX6627)
- Operating Temperature Range (-55°C to +125°C)
- Measurement Temperature Range, Remote Junction (-55°C to +145°C)
- 0.5s (MAX6627) or 8s (MAX6628) Conversion Rate
- SPI-Compatible Interface
- +3.0V to +5.5V Supply Range
- 8-Pin SOT23 Package

Ordering Information

PART	TEMP RANGE	PIN- PACKAGE	TOP MARK
MAX6627MKA-T	-55°C to +125°C	8 SOT23-8	AAEQ
MAX6628MKA-T	-55°C to +125°C	8 SOT23-8	AAER

Typical Operating Circuit



SPI is a trademark of Motorola, Inc.

Pin Configuration appears at end of data sheet.

M/IXI/M

Maxim Integrated Products 1

For pricing, delivery, and ordering information, please contact Maxim/Dallas Direct! at

ABSOLUTE MAXIMUM RATINGS

All Voltages Referenced to GND

V _{CC}	
SO, SCK, DXP, CS	0.3V to V _{CC} + 0.3V
DXN	
SO Pin Current Range	1mA to +50mA
Current Into All Other Pins	10mA
ESD Protection (Human Body Model)	2000V

Continuous Power Dissipation ($T_A = +70^{\circ}C$)
8-Pin SOT23 (derate 9.7mW/°C above +70°C)777mW
Operating Temperature Range55°C to +125°C
Junction Temperature+150°C
Storage Temperature Range65°C to +150°C
Lead Temperature (soldering, 10s)+300°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

 $(3.0V \le V_{CC} \le 5.5V, -55^{\circ}C \le T_A \le +125^{\circ}C, unless otherwise noted. Typical values are at T_A = +25^{\circ}C, V_{CC} = +3.3V, unless otherwise noted.)$

PARAMETER	SYMBOL	CONDITIONS	MIN	ТҮР	MAX	UNITS	
TEMPERATURE	•						
		$0^{\circ}C \le T_{RJ} \le +125^{\circ}C, T_A = +30^{\circ}C, V_{CC} = +3.3V$	-1.0	±0.5	±1		
		$\label{eq:rescaled_states} \begin{array}{l} -55^\circ C \leq T_{RJ} \leq +100^\circ C, \ 0^\circ C \leq T_A \leq +70^\circ C, \\ V_{CC} = +3.3 V \end{array}$	-2.4		+2.4	- °C	
Accuracy (Note 1)		$\label{eq:transform} \begin{array}{l} -55^\circ\text{C} \leq \text{T}_{\text{RJ}} \leq +145^\circ\text{C}, \ 0^\circ\text{C} \leq \text{T}_{\text{A}} \leq +70^\circ\text{C}, \\ \text{V}_{\text{CC}} = +3.3\text{V} \end{array}$	-4.5		+4.5		
		$\label{eq:rescaled} \begin{array}{l} -55^\circ\text{C} \leq \text{T}_{\text{RJ}} \leq +125^\circ\text{C}, \ -55^\circ\text{C} \leq \text{T}_{\text{A}} \leq +125^\circ\text{C}, \\ \text{V}_{\text{CC}} = +3.3\text{V} \end{array}$	-5.5		+5.5		
Power-Supply Sensitivity				0.25	0.7	°C/V	
Resolution				0.0625		°C	
Time Between Conversion Starts	town	MAX6627		0.5		s	
Time between Conversion Starts	^t SAMPLE	MAX6628	8			3	
Conversion Time	tCONV		180	250	320	ms	
POWER SUPPLY							
Supply Voltage Range	Vcc		3.0		5.5	V	
	I _{SD}	Shutdown, $V_{CC} = +0.8V$			5		
Supply Current, SCK Idle	IIDLE	ADC idle, \overline{CS} = low			20	μA	
	ICONV	ADC converting		360	600		
Average Operating Current		MAX6627		200	400		
Average Operating Current	lcc	MAX6628		30	50	μA	
Power-On Reset (POR) Threshold		V _{CC} , falling edge		1.6		V	
Current Sourcing for Diada		High level	80	100	120		
Current Sourcing for Diode		Low level		10	12	μA	

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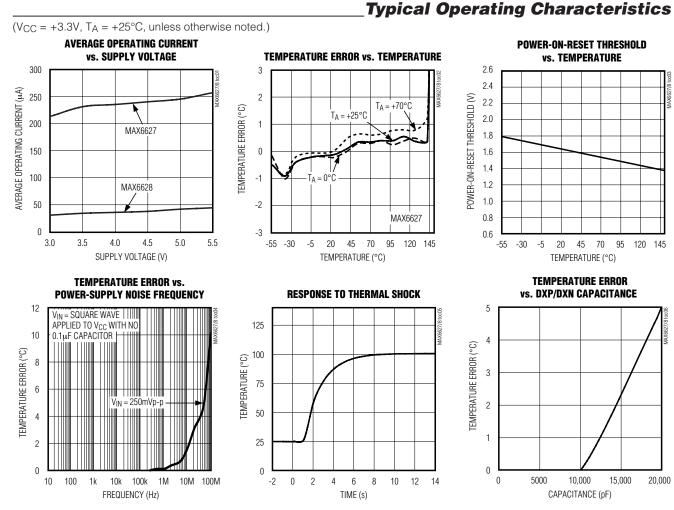
ELECTRICAL CHARACTERISTICS (continued)

 $(3.0V \le V_{CC} \le 5.5V, -55^{\circ}C \le T_A \le +125^{\circ}C, unless otherwise noted. Typical values are at T_A = +25^{\circ}C, V_{CC} = +3.3V, unless otherwise noted.)$

PARAMETER	SYMBOL	CONDITIONS	MIN	ТҮР	МАХ	UNITS
LOGIC INPUTS (CS, SCK)			I			1
Logic Input Low Voltage	VIL				0.3 x V _{CC}	V
Logic Input High Voltage	VIH		0.7 x V _{CC}			V
Input Leakage Current	ILEAK	$V_{\overline{CS}} = V_{SCK} = GND \text{ or } V_{CC}$			1	μA
LOGIC OUTPUTS (SO)						
Output Low Voltage	Vol	I _{SINK} = 1.6mA			0.4	
Output High Voltage	V _{OH}	ISOURCE = 1.6mA	V _{CC} - 0.4			V
TIMING CHARACTERISTICS (1	Note 2, Figure	2)				
Serial Clock Frequency	fscl				5	MHz
SCK Pulse Width High	tСН		100			ns
SCK Pulse Width Low	tCL		100			ns
CS Fall to SCK Rise	tcss	$C_{LOAD} = 10 pF$	80			ns
CS Fall to Output Enable	t _{DV}	$C_{LOAD} = 10 pF$			80	ns
CS Rise to Output Disable	t _{TR}	$C_{LOAD} = 10 pF$			50	ns
SCK Fall to Output Data Valid	tDO	$C_{LOAD} = 10 pF$			80	ns

Note 1: T_{RJ} is the temperature of the remote junction.

Note 2: Serial timing characteristics guaranteed by design.



Pin Description

PIN	NAME	FUNCTION
1	GND	Ground
2	DXN	Combined Current Sink and ADC Negative Input for Remote Diode. DXN is normally biased to a diode voltage above ground.
3	DXP	Combined Current Source and ADC Positive Input for Remote Diode
4	Vcc	Supply Voltage Input. Bypass with a 0.1µF to GND.
5	SCK	SPI Clock Input
6	CS	Chip Select Input. Pulling \overline{CS} low initiates an idle state, but the SPI interface is still enabled. A rising edge of \overline{CS} initiates the next conversion.
7	SO	SPI Data Output
8	N.C.	No Connect. Can be connected to GND for improved thermal conductivity.

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MAX6627/MAX6628

Detailed Description

The MAX6627/MAX6628 remote digital thermometers report the temperature of a remote sensor. The remote sensor is a diode-connected transistor-typically, a low-cost, easily mounted 2N3904 NPN type-that replaces conventional thermistors or thermocouples. The MAX6627/MAX6628 can also measure the die temperature of other ICs, such as µPs or µCs, that contain an on-chip, diode-connected transistor.

Remote accuracy is ±1°C when the temperature of the remote diode is between 0°C and +125°C and the temperature of the MAX6627/MAX6628 is +30°C. Data is available as a 12-bit + sign word with 0.0625°C resolution. The operating range of the device extends from -55°C to +125°C, although the architecture of the device is capable of interpreting data up to +145°C. The device itself should never exceed +125°C.

The MAX6627/MAX6628 are designed to work in conjunction with an external µC or other intelligent device serving as the master in thermostatic, process-control, or monitoring applications. The μ C is typically a power management or keyboard controller, generating SPI serial commands by "bit-banging" GPIO pins.

Two conversion rates are available; the MAX6627 continuously converts data every 0.5s, and the MAX6628 continuously converts data every 8s. Either device can be read at any time and provide the data from the last conversion. The slower version provides minimal power consumption under all operating conditions. Or, by taking \overline{CS} low, any conversion in progress is stopped, and the rising edge of \overline{CS} always starts a fresh conversion and resets the interface. This permits triggering a conversion at any time so that the power consumption of the MAX6627 can be overcome, if needed. Both devices operate with input voltages between +3.0V and +5.5V and are specified between -55°C and +125°C. The MAX6627 and MAX6628 come in space-saving 8pin SOT23 packages.

ADC Conversion Sequence

The device powers up as a free-running data converter (Figure 1). The \overline{CS} pin can be used for conversion control. The rising edge of \overline{CS} resets the interface and starts a conversion. The falling edge of \overline{CS} stops any conversion in progress, overriding the latency of the part. Temperature data from the previous completed conversion is available for read (Tables 1 and 2). It is required to maintain \overline{CS} high for a minimum of 320ms to complete a conversion.

Idle Mode

Pull \overline{CS} low to enter idle mode. In idle mode, the ADC is not converting. The serial interface is still active and temperature data from the last completed conversion can still be read.

Power-On Reset

The POR supply voltage of the MAX6627/MAX6628 is typically 1.6V. Below this supply voltage, the interface is inactive and the data register is set to the POR state,

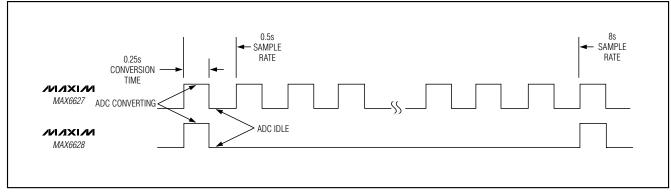


Figure 1. Free-Running Conversion Time and Rate Relationships

Table 1. Data Output Format

///XI///

D15	D14	D13	D12	D11	D10	D9	D8	D7	D6	D5	D4	D3	D2	D1	D0
Sign	MSB Data											LSB Data	Low	High-Z	High-Z

MAX6627/MAX6628

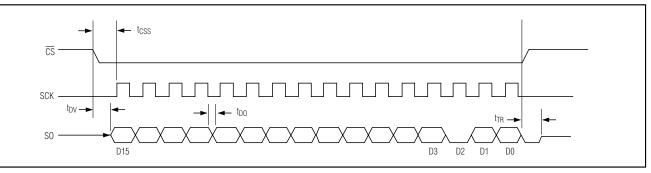


Figure 2. SPI Timing Diagram

Table 2. Temperature Data Format(Two's Complement)

TEMPERATURE	DIGITAL OUTPUT (BINARY)							
(°C)	D15–D3	D2	D1, D0					
150	0,1001,0110,0000	0	XX					
125	0,0111,1101,0000	0	XX					
25	0,0001,1001,0000	0	XX					
0.0625	0,0000,0000,0001	0	XX					
0	0,0000,0000,0000	0	XX					
-0.0625	1,1111,1111,1111	0	XX					
-25	1,1110,0111,0000	0	XX					
-55	1,1100,1001,0000	0	XX					

0°C. When power is first applied and V_{CC} rises above 1.6V (typ), the device starts to convert, although temperature reading is not recommended at V_{CC} levels below 3.0V.

Serial Interface

Figure 2 is the serial interface timing diagram. The data is latched into the shift register on the falling edge of the \overline{CS} signal and then clocked out at the SO pin on the falling edge of SCK with the most-significant bit (MSB) first. There are 16 edges of data per frame. The last 2 bits, D0 and D1, are always in high-Z mode. The falling edge of \overline{CS} stops any conversion in progress, and the rising edge of \overline{CS} always starts a new conversion and resets the interface. It is required to maintain a 320ms minimum pulse width of high \overline{CS} signal before a conversion starts.

Applications Information

Remote-Diode Selection

Temperature accuracy depends upon having a goodquality, diode-connected, small-signal transistor. Accuracy has been experimentally verified for all of the devices listed in Table 3. The MAX6627/MAX6628 can also directly measure the die temperature of CPUs and other ICs with on-board temperature-sensing diodes.

The transistor must be a small-signal type with a relatively high forward voltage. This ensures that the input voltage is within the A/D input voltage range. The forward voltage must be greater than 0.25V at 10µA at the highest expected temperature. The forward voltage must be less than 0.95V at 100µA at the lowest expected temperature. The base resistance has to be less than 100 Ω . Tight specification of forward-current gain (+50 to +150, for example) indicates that the manufacturer has good process control and that the devices have consistent characteristics.

ADC Noise Filtering

The integrating ADC has inherently good noise rejection, especially of low-frequency signals such as 60Hz/120Hz power-supply hum. Micropower operation places constraints on high-frequency noise rejection. Lay out the PC board carefully with proper external noise filtering for high-accuracy remote measurements in electrically noisy environments.

Table 3. SOT23-Type Remote-SensorTransistor Manufacturers

MANUFACTURER	MODEL
Central Semiconductor (USA)	CMPT3904
Fairchild Semiconductor (USA)	MMBT3904
Motorola (USA)	MMBT3904
Rohm Semiconductor (Japan)	SST3904
Siemens (Germany)	SMBT3904
Zetex (England)	FMMT3904CT-ND

Note: Transistors must be diode connected (short the base to the collector).



MAX6627/MAX6628

Remote ±1°C Accurate Digital Temperature Sensors with SPI-Compatible Serial Interface

Filter high-frequency electromagnetic interference (EMI) at DXP and DXN with an external 2200pF capacitor connected between the two inputs. This capacitor can be increased to about 3300pF (max), including cable capacitance. A capacitance higher than 3300pF introduces errors due to the rise time of the switched-current source.

PC Board Layout

- Place the MAX6627/MAX6628 as close as practical to the remote diode. In a noisy environment, such as a computer motherboard, this distance can be 4in to 8in, or more, as long as the worst noise sources (such as CRTs, clock generators, memory buses, and ISA/PCI buses) are avoided.
- 2) Do not route the DXP/DXN lines next to the deflection coils of a CRT. Also, do not route the traces across a fast memory bus, which can easily introduce +30°C error, even with good filtering. Otherwise, most noise sources are fairly benign.
- Route the DXP and DXN traces parallel and close to each other, away from any high-voltage traces such as +12VDC. Avoid leakage currents from PC board contamination. A 20MΩ leakage path from DXP to ground causes approximately +1°C error.
- Connect guard traces to GND on either side of the DXP/DXN traces (Figure 3). With guard traces in place, routing near high-voltage traces is no longer an issue.
- 5) Route as few vias and crossunders as possible to minimize copper/solder thermocouple effects.
- 6) When introducing a thermocouple, make sure that both the DXP and the DXN paths have matching thermocouples. In general, PC board-induced thermocouples are not a serious problem. A copper solder thermocouple exhibits 3µV/°C, and it takes approximately 200µV of voltage error at DXP/DXN to cause a +1°C measurement error, so most parasitic thermocouple errors are swamped out.
- 7) Use wide traces. Narrow traces are more inductive and tend to pick up radiated noise. The 10mil widths and spacings recommended in Figure 3 are not absolutely necessary (as they offer only a minor improvement in leakage and noise), but use them where practical.
- Placing an electrically clean copper ground plane between the DXP/DXN traces and traces carrying high-frequency noise signals helps reduce EMI.

Twisted Pair and Shielded Cables

For remote-sensor distances longer than 8in, or in particularly noisy environments, a twisted pair is recommended. Its practical length is 6ft to 12ft (typ) before noise becomes a problem, as tested in a noisy electronics laboratory. For longer distances, the best solution is a shielded twisted pair like that used for audio microphones. For example, Belden #8451 works well for distances up to 100ft in a noisy environment. Connect the twisted pair to DXP and DXN and the shield to ground, and leave the shield's remote end unterminated. Excess capacitance at DXN or DXP limits practical remote-sensor distances (see *Typical Operating Characteristics*).

For very long cable runs, the cable's parasitic capacitance often provides noise filtering, so the recommended 2200pF capacitor can often be removed or reduced in value. Cable resistance also affects remote-sensor accuracy. A 1 Ω series resistance introduces about +1/2°C error.

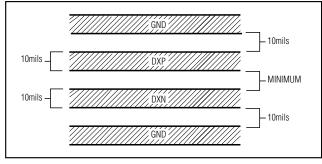
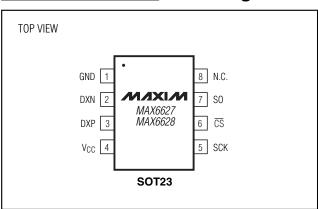
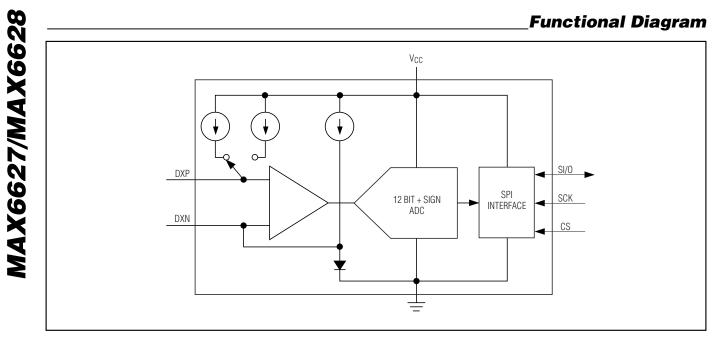


Figure 3. Recommended DXP/DXN PC Traces



Pin Configuration

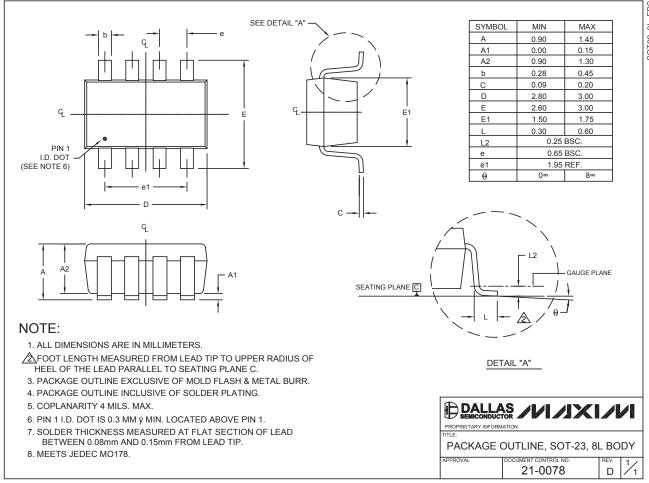


Chip Information

TRANSISTOR COUNT: 6241 PROCESS: BICMOS

Package Information

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information, go to **www.maxim-ic.com/packages**.)



MAX6627/MAX6628

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